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7590 04/13/2004		EXAMINER			
Mark E Fejer			VARTANIAN, HARRY		
Pauley Petersen			- Indiana	DARED NUMBER	
2800 West Higg	ins Road	ART UNIT	PAPER NUMBER		
Suite 365		2634	8		
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Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)	
•		09/677,284	GOODMAN E	ΓAL.
Office Action Sum	mary	Examiner	Art Unit	:
		Harry Vartanian	2634	
	communication app	ears on the cover sh	eet with the correspondence	address
Period for Reply A SHORTENED STATUTORY F THE MAILING DATE OF THIS C - Extensions of time may be available under after SIX (6) MONTHS from the mailing dat - If the period for reply specified above is less - If NO period for reply is specified above, the - Failure to reply within the set or extended p Any reply received by the Office later than t earned patent term adjustment. See 37 CF	communication. the provisions of 37 CFR 1.13 e of this communication. than thirty (30) days, a reply maximum statutory period weriod for reply will, by statute, hree months after the mailing	36(a). In no event, however, within the statutory minimularill apply and will expire SIX cause the application to be	may a reply be timely filed m of thirty (30) days will be considered t (6) MONTHS from the mailing date of th come ABANDONED (35 U.S.C. § 133).	nis communication.
Status				; !
 1) Responsive to communicate 2a) This action is FINAL. 3) Since this application is in closed in accordance with 	2b)⊠ This condition for allowan	action is non-final. ace except for forma	al matters, prosecution as to 15 C.D. 11, 453 O.G. 213.	the merits is
Disposition of Claims				•
4) ⊠ Claim(s) <u>1-10</u> is/are pending 4a) Of the above claim(s) is/are allow 5) □ Claim(s) <u>1-8</u> is/are rejected 7) ⊠ Claim(s) <u>9 and 10</u> is/are old 8) □ Claim(s) <u></u> are subjective.	is/are withdraw wed. d. pjected to.	vn from consideratio		
Application Papers				:
• • • •	February 2004 is/are at any objection to the cost including the correction	: a)⊠ accepted or drawing(s) be held in a on is required if the di	abeyance. See 37 CFR 1.85(a rawing(s) is objected to. See 37). ' CFR 1.121(d).
Priority under 35 U.S.C. § 119				
3. Copies of the certifie	lone of: ne priority documents ne priority documents nd copies of the priori International Bureau	s have been receive s have been receive ity documents have (PCT Rule 17.2(a)	d. d in Application No been received in this Nation).	nal Stage
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Attachment(s)				:
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawin 3) Information Disclosure Statement(s) (P Paper No(s)/Mail Date		Pap	erview Summary (PTO-413) er No(s)/Mail Date ice of Informal Patent Application (er:	PTO-152)

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Detailed Action

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 1. Claims 1, 2, 3 are rejected under 35 U.S.C. 102(b) as being anticipated by Petranovich(US Patent 5,625,652). Regarding Claim 1, Petranovich meets the following limitations of the Claim:

In a digital communications system having a transmitter for transmitting information in a form of a phase shift keyed signal, said signal being divided into a plurality of windows(fig 9), each said window being offset in time(fig 9), and a receiver for receiving said information, a demodulator comprising:

conversion means for converting said phase shift keyed signal(column 4, lines 23-30) into a plurality of successive outputs(Column 8, Lines 11-21),

each said successive output being representative of a phase and amplitude of said phase shift keyed signal over additional sets of windows and carrier phases, and each said additional set having different offsets in time and carrier phase from the other said additional sets; (Column 4, lines 49-64); (Column 8, Lines 22-35)

estimation means for using said plurality of signals to estimate said carrier phase and said window offsets of said phase shift keyed signal, wherein said carrier phase and window offsets are synchronous; and(Column 4, lines 23-30), (column 16, lines 38-62)

decoding means for decoding said phase shift keyed signal back into digital data using said estimates. **Abstract**

More specifically, Petranovich shows the use of windowing to lock onto the timing of the incoming signal. With each incoming burst, the window is adjusted so that the bits are recovered in the right position and the value of the carrier phase is correct. Therefor the carrier phase and window offsets are synchronized. This is necessary in PSK in order to detect the correct symbols, especially in higher order PSK (for example QPSK) where slight offsets can cause the demodulator to decode the wrong symbol.

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Regarding Claim 2, the reason for rejection of Claim 1 above also applies here.

Regarding Claim 3, Petranovich meets the following limitations of the Claim:

wherein a distribution of bit values received for a set of possible said window offsets and carrier phases is measured and a most likely said window offset and carrier phase are selected. (Column 8, Lines 26-35); Claims 1 and 2;

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

- (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 1. Claim 4 is rejected under 35 U.S.C. 103(a) as being unpatentable over Petranovich(US Patent 5,625,652) in view of Matsumoto(US Pat #4,763,331). Petranovich meets all the limitations of Claim 4(Please see above paragraphs) expect the use of maximum increasing function of absolute value for window offset estimation.

However, Matsumoto discloses a "...decoding method according to claim 3, wherein the reliability information is a monotonic increasing function of the absolute value of a difference between the two summations."(Claim 5) Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made that Petranovich PSK detector use a maximum increasing function of absolute value for window offset estimation in his receiver. The motivation to do combine is that increasing function of absolute value is a common statistical measurement method to measure error.

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2. Claims 5 and 8 are rejected under 35 U.S.C. 103(a) as being unpatentable over Petranovich(US Patent 5,625,652) in view of Eastmond et al(US Pat #6,088,337). Petranovich meets all the limitations of Claim 5(Please see above paragraphs) expect the use of maximum sum of squares for window offset estimation.

However, Eastmond et al discloses "a function of the sum of the squares of the outof-phase autocorrelation, are commonly used to determine good synchronization
words"(Table 2) for his invention. Regarding the limitation of comparing the sum of squares
to a "theoretical" value, Petranovich discloses that the phase error is compared to a
predetermined threshold. Therefore, it would have been obvious to one of ordinary skill in
the art at the time the invention was made that Petranovich's PSK detector use sum of
squares for measuring and comparing the results of his window offset estimation. The
motivation to do combine is sum of squares is a common statistical method to measure
error.

3. Claim 6 is rejected under 35 U.S.C. 103(a) as being unpatentable over Petranovich(US Patent 5,625,652) in view of Kumar(US Pat #5,966,401). Petranovich meets all the limitations of Claim 6(Please see above paragraphs) expect the use of maximum sum of absolute values for his window and carrier phase detection.

However, Kumar discloses a receiver where "the correlation sums 31 and 33 are propagated to the absolute-value functions 35 and 37, respectively, which remove the polarity information from the correlation sums by discarding sign-bit information."(Column 7, Lines 1-5) Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made that Petranovich's PSK detector use a maximum sum of absolute values for measuring and comparing the results of his window offset estimation.

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The motivation to do combine is sum of absolute values is a common statistical method to

measure the error of a number.

4. Claim 7 is rejected under 35 U.S.C. 103(a) as being unpatentable over

Petranovich(US Patent 5,625,652) in view of Shenoy et al(US Pat #5,297,172).

Petranovich meets all the limitations of Claim 7(Please see above paragraphs) expect the

step of minimizing BER for his window and carrier phase detection.

However, Shenoy et al discloses a PSK receiver where "In almost all digital data

transmission systems, the receiver is required to sample the received waveform once per

symbol in a relatively small interval in order to minimize the bit-error-rate (BER)." (Column

1, Lines 15-22) Therefore, it would have been obvious to one of ordinary skill in the art at

the time the invention was made that Petranovich's PSK detector use a minimization of

BER for measuring and comparing the results of his window offset estimation. The

motivation to do combine is BER is common metric used to determine the quality of a

received signal and decoding method.

Allowable Subject Matter

5. Claims 9 and 10 are objected to as being dependent upon a rejected base claim, but

would be allowable if rewritten in independent form including all of the limitations of the

base claim and any intervening claims.

Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Harry Vartanian whose telephone number is 703.305.8698.

The examiner can normally be reached on 9-5:30 Mondays to Fridays.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Stephen Chin can be reached on 703.305.4714. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Harry Vartanian Examiner Art Unit 2634

HV

STEPHEN CHIN
SUPERVISORY PATENT EXAMINE

TECHNOLOGY CENTER 2600